

WHAT IS CLAIMED IS:

1. An electronic apparatus to which a plurality of batteries are detachably mounted, comprising:

5           a removal requirement receipt section for receiving a removal requirement for a part of the mounted batteries;

10           a processing ability determination section responsive to the removal requirement for a battery from said removal requirement receipt section for determining whether a supplying possible electric power from the remaining batteries is an electric power capable of maintaining a processing ability or an electric power which needs to lower the processing ability; and

15           a processing ability control section for lowering the processing ability in accordance with a decision from said processing ability determination section that the electric power needs to lower the processing ability.

20           2. An electronic apparatus to which a plurality of batteries are detachably mounted, comprising:

          a removal requirement receipt section for receiving a removal requirement for a part of the mounted batteries; and

25           a processing ability control section responsive to the removal requirement for a battery from said removal requirement receipt section for lowering a processing

ability.

3. An electronic apparatus to which a plurality of batteries are detachably mounted, comprising:

5 a mounting and removal detection section for detecting mounting and removal of batteries; and

a processing ability control section responsive to a detection of a removal of a battery by said mounting and removal detection section for lowering a processing ability.

10 4. An electronic apparatus according to claim 1, in which said electronic apparatus has a portion receiving a clock and operative in synchronism with the clock while consuming an electronic power according to a repetitive  
15 frequency of the clock,

wherein said processing ability control section changes over the frequency of the clock to control the processing ability.

20 5. An electronic apparatus according to claim 2, in which said electronic apparatus has a portion receiving a clock and operative in synchronism with the clock while consuming an electronic power according to a repetitive  
frequency of the clock,

25 wherein said processing ability control section changes over the frequency of the clock to control the processing ability.

6. An electronic apparatus according to claim 3,  
in which said electronic apparatus has a portion receiving  
a clock and operative in synchronism with the clock while  
5 consuming an electronic power according to a repetitive  
frequency of the clock,

wherein said processing ability control section  
changes over the frequency of the clock to control the  
processing ability.

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7. An electronic apparatus according to claim 1,  
wherein said processing ability determination section  
receives the removal requirement for a battery from said  
removal requirement receipt section and determines whether  
15 an electric power supplying ability is insufficient with  
only the remaining batteries, even if the processing  
ability is lowered by said processing ability control  
section, and

said electronic apparatus further comprises a  
20 removal acceptance display section for displaying inhibit  
or acceptance of the removal of a battery according as said  
processing ability determination section determines whether  
an electric power supplying ability is insufficient with  
only the remaining batteries, even if the processing  
25 ability is lowered by said processing ability control  
section.

8. An electronic apparatus according to claim 2,  
further comprising:

a processing ability determination section  
responsive to the removal requirement for a battery from  
said removal requirement receipt section for determining  
whether an electric power supplying ability is insufficient  
with only the remaining batteries, even if the processing  
ability is lowered by said processing ability control  
section, and

a removal acceptance display section for  
displaying inhibit or acceptance of the removal of a  
battery according as said processing ability determination  
section determines whether an electric power supplying  
ability is insufficient with only the remaining batteries,  
even if the processing ability is lowered by said  
processing ability control section.

9. An electronic apparatus according to claim 1,  
further comprising a residual electric power monitor  
section for monitoring a residual electric power of the  
mounted batteries.

10. An electronic apparatus according to claim 2,  
further comprising a residual electric power monitor  
section for monitoring a residual electric power of the  
mounted batteries.

11. An electronic apparatus according to claim 9,  
wherein said residual electric power monitor section  
measures voltage and supplying current of the mounted  
batteries and determines a residual electric power of the  
5 batteries through an arithmetic operation.

12. An electronic apparatus according to claim 10,  
wherein said residual electric power monitor section  
measures voltage and supplying current of the mounted  
10 batteries and determines a residual electric power of the  
batteries through an arithmetic operation.

13. An electronic apparatus according to claim 1,  
wherein each of said batteries is a chargeable secondary  
15 battery provided in a battery pack, and a plurality of such  
battery packs are capable of being mounted on said  
electronic apparatus.

14. An electronic apparatus according to claim 2,  
20 wherein each of said batteries is a chargeable secondary  
battery provided in a battery pack, and a plurality of such  
battery packs are capable of being mounted on said  
electronic apparatus.

25 15. An electronic apparatus according to claim 3,  
wherein each of said batteries is a chargeable secondary  
battery provided in a battery pack, and a plurality of such

battery packs are capable of being mounted on said electronic apparatus.

16. An electronic apparatus according to claim 1,  
5 wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory for storing a residual electric power of a battery of an associated battery pack.

10 17. An electronic apparatus according to claim 2,  
wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of  
15 said battery packs has a memory for storing a residual electric power of a battery of an associated battery pack.

18. An electronic apparatus according to claim 9,  
wherein each of said batteries is a battery provided in a  
20 battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory for storing an association between voltage and supplying current of an associated battery and a residual electric power of the battery, and  
25 wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power

of the batteries referring to said memories.

19. An electronic apparatus according to claim 10,  
wherein each of said batteries is a battery provided in a  
battery pack, a plurality of such battery packs are capable  
of being mounted on said electronic apparatus, and each of  
said battery packs has a memory for storing an association  
between voltage and supplying current of an associated  
battery and a residual electric power of the battery, and

wherein said residual electric power monitor  
section measures voltage and supplying current of the  
mounted batteries and determines a residual electric power  
of the batteries referring to said memories.

20. An electronic apparatus according to claim 11,  
wherein each of said batteries is a battery provided in a  
battery pack, a plurality of such battery packs are capable  
of being mounted on said electronic apparatus, and each of  
said battery packs has a memory for storing an association  
between voltage and supplying current of an associated  
battery and a residual electric power of the battery, and

wherein said residual electric power monitor  
section measures voltage and supplying current of the  
mounted batteries and determines a residual electric power  
of the batteries referring to said memories.

21. An electronic apparatus according to claim 12,

wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory for storing an association  
5 between voltage and supplying current of an associated battery and a residual electric power of the battery, and

wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power  
10 of the batteries referring to said memories.

22. An electronic apparatus according to claim 1, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable  
15 of being mounted on said electronic apparatus, and each of said battery packs has a memory for storing an association between a residual electric power of an associated battery and a maximum chargeable current, and

wherein said processing ability determination  
20 section performs a determination referring to said memories.

23. An electronic apparatus according to claim 7, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable  
25 of being mounted on said electronic apparatus, and each of said battery packs has a memory for storing an association between a residual electric power of an associated battery



and a maximum chargeable current, and

wherein said processing ability determination section performs a determination referring to said memories.

5           24. An electronic apparatus according to claim 8,  
wherein each of said batteries is a battery provided in a  
battery pack, a plurality of such battery packs are capable  
of being mounted on said electronic apparatus, and each of  
said battery packs has a memory for storing an association  
10 between a residual electric power of an associated battery  
and a maximum chargeable current, and

wherein said processing ability determination section performs a determination referring to said memories.

15           25. A processing ability alteration instruction  
apparatus for instructing an alteration of a processing  
ability to an electronic apparatus to which a plurality of  
batteries are detachably mounted, comprising:

20           a removal requirement receipt section for  
receiving a removal requirement for a part of the batteries  
mounted on said electronic apparatus;

25           a processing ability determination section  
responsive to the removal requirement for a battery from  
said removal requirement receipt section for determining  
whether a supplying possible electric power from the  
remaining batteries only is an electric power capable of  
maintaining a processing ability or an electric power which

needs to lower the processing ability; and

a processing ability alteration instruction  
section for instructing said electronic apparatus to lower  
the processing ability in accordance with a decision from  
5 said processing ability determination section that the  
electric power needs to lower the processing ability.

26. A processing ability alteration instruction  
apparatus for instructing an alteration of a processing  
10 ability to an electronic apparatus to which a plurality of  
batteries are detachably mounted, comprising:

a removal requirement receipt section for  
receiving a removal requirement for a part of the batteries  
mounted on said electronic apparatus; and

15 a processing ability alteration instruction  
section responsive to the removal requirement for a battery  
from said removal requirement receipt section for  
instructing said electronic apparatus to lower the  
processing ability.

20 27. A processing ability alteration instruction  
apparatus for instructing an alteration of a processing  
ability to an electronic apparatus to which a plurality of  
batteries are detachably mounted, comprising:

25 a mounting and removal detection section for  
detecting mounting and removal of batteries on and from  
said electronic apparatus; and

5 a processing ability alteration instruction  
section responsive to a detection of a removal of a battery  
by said mounting and removal detection section for  
instructing said electronic apparatus to lower the  
processing ability.

10 28. A processing ability alteration instruction  
apparatus according to claim 25, wherein said processing  
ability determination section receives the removal  
requirement for a battery from said removal requirement  
receipt section and determines whether an electric power  
supplying ability is insufficient with only the remaining  
batteries, even if the processing ability of said  
electronic apparatus is lowered, and  
15 said processing ability alteration instruction  
apparatus further comprises a removal acceptance display  
section for displaying inhibit or acceptance of the removal  
of a battery according as said processing ability  
determination section determines whether an electric power  
20 supplying ability is insufficient with only the remaining  
batteries, even if the processing ability is lowered.

25 29. A processing ability alteration instruction  
apparatus according to claim 26, further comprising:  
a processing ability determination section  
responsive to the removal requirement for a battery from  
said removal requirement receipt section for determining

whether an electric power supplying ability is insufficient with only the remaining batteries, even if the processing ability of said electronic apparatus is lowered, and

5 a removal acceptance display section for displaying inhibit or acceptance of the removal of a battery according as said processing ability determination section determines whether an electric power supplying ability is insufficient with only the remaining batteries, even if the processing ability of said electronic apparatus  
10 is lowered.

30. A processing ability alteration instruction apparatus according to claim 25, further comprising a residual electric power monitor section for monitoring a  
15 residual electric power of the mounted batteries.

31. A processing ability alteration instruction apparatus according to claim 26, further comprising a residual electric power monitor section for monitoring a  
20 residual electric power of the mounted batteries.

32. A processing ability alteration instruction apparatus according to claim 30, wherein said residual electric power monitor section measures voltage and  
25 supplying current of the mounted batteries and determines a residual electric power of the batteries through an arithmetic operation.

33. A processing ability alteration instruction apparatus according to claim 31, wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power of the batteries through an arithmetic operation.

34. A processing ability alteration instruction apparatus according to claim 25, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory for storing a residual electric power of a battery of an associated battery pack.

35. A processing ability alteration instruction apparatus according to claim 26, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory for storing a residual electric power of a battery of an associated battery pack.

36. A processing ability alteration instruction apparatus according to claim 30, wherein each of said batteries is a battery provided in a battery pack, a

plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory for storing an association between voltage and supplying current of an associated battery and a residual electric power of the battery, and wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power of the batteries referring to said memories.

37. A processing ability alteration instruction apparatus according to claim 31, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory for storing an association between voltage and supplying current of an associated battery and a residual electric power of the battery, and wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power of the batteries referring to said memories.

38. A processing ability alteration instruction apparatus according to claim 32, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being

mounted on said electronic apparatus, and each of said battery packs has a memory for storing an association between voltage and supplying current of an associated battery and a residual electric power of the battery, and

5            wherein said residual electric power monitor section measures voltage and supplying current of the mounted batteries and determines a residual electric power of the batteries referring to said memories.

10            39. A processing ability alteration instruction apparatus according to claim 33, wherein each of said batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said  
15            battery packs has a memory for storing an association between voltage and supplying current of an associated battery and a residual electric power of the battery, and

             wherein said residual electric power monitor section measures voltage and supplying current of the  
20            mounted batteries and determines a residual electric power of the batteries referring to said memories.

             40. A processing ability alteration instruction apparatus according to claim 25, wherein each of said  
25            batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said

battery packs has a memory for storing an association between a residual electric power of an associated battery and a maximum chargeable current, and

5            wherein said processing ability determination section performs a determination referring to said memories.

41. A processing ability alteration instruction apparatus according to claim 28, wherein each of said  
10       batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory for storing an association between a residual electric power of an associated battery and a maximum chargeable current, and  
15            wherein said processing ability determination section performs a determination referring to said memories.

42. A processing ability alteration instruction apparatus according to claim 29, wherein each of said  
20       batteries is a battery provided in a battery pack, a plurality of such battery packs are capable of being mounted on said electronic apparatus, and each of said battery packs has a memory for storing an association between a residual electric power of an associated battery  
25       and a maximum chargeable current, and  
             wherein said processing ability determination section performs a determination referring to said memories.